RESPONSE TO RESTRICTION REQUIREMENT

Sérial Number: 09/945495 Filing Date: August 30, 2001

Title: VERTICAL TRANSISTOR AND METHOD OF MAKING

Page 2 Dkt: 303.776US1

## **REMARKS**

Applicant respectfully asserts that the claims are now in condition for examination.

The Examiner is invited to contact Applicants' Representative John Greaves at 801/278-9171 or the below signed attorney if there are any questions regarding this Response or if prosecution of this application may be assisted thereby.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

FERNANDO GONZALEZ

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

612-373-6904

Date _	Feb	19	2003	_ By _	V	an	أسا		$\int_{-\infty}^{\infty}$	/	Klitz	/
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Reg No. 32 146												

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on this \_\_\_\_\_\_\_ day of February, 2003.

Name Signature